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THE UNITED STATES PATENT AND TRADEMARK OFFICE

DOCKET NO.: BUR920030051US1

In re Application of:  
**BAJUK ET AL.**

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Examiner: **TUYEN P. TO**

Serial No.: **10/604,071**

Art Unit: **2825**

Filed: **JUNE 25, 2003**

**Confirmation No. 1070**

For: **CODING OF FPGA AND  
STANDARD CELL LOGIC IN A  
TILING STRUCTURE**

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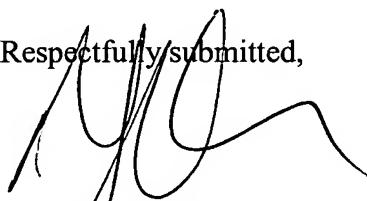
**LETTER TO THE OFFICIAL DRAFTSMAN**

**DRAWING REVIEW BRANCH**  
U. S. Patent and Trademark Office  
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith are eight (8) *replacement* drawings with corrections pursuant to the Draftsman's Drawing Review in the above-referenced allowed application.

Respectfully submitted,



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3/29/06  
(Date)

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